

IN THE DRAWINGS

Please amend Figures 11 and 12 to include the label "Prior Art". Replacement sheets are submitted herewith including the amendments to Figures 11 and 12.

currents, depending on a potential of the oscillation frequency control signal, and the gain of the charge pump circuit is continuously controlled, depending on a sum of the currents flowing through the plurality of transistors. At least these features are not disclosed or even suggested by Momtaz.

The Examiner equates the charge pump current generator 44, depicted in Figures 2 and 3 of Momtaz, with the linearization circuit of claim 12, and equates transistors 60 (Figure 3) with the plurality of transistors recited in the claim. However, the transistors 60 do not receive the oscillation frequency control signal from the loop filter and change flowing currents depending on a potential of the oscillation frequency control signal, as recited in claim 12. As clearly depicted in Figure 3, only transistor X4 receives the control voltage Vc from analog filter 40. Thus, Momtaz does not disclose a plurality of transistors which receive the oscillation frequency control signal from the loop filter, as recited in claim 12. Because Momtaz fails to disclose a plurality of transistors as recited in claim 12, Momtaz cannot disclose controlling the gain of the charge pump depending on the sum of currents flowing through the plurality of transistors.

Claim 14 recites, among other things, a PLL frequency synthesizer comprising a linearization circuit which has a transistor for receiving the oscillation frequency control signal from the loop filter and changing a flowing current, depending on a potential of the oscillation frequency control signal and a bias voltage generating circuit for generating a bias voltage, and wherein a source voltage of the transistor of the linearization circuit is controlled to be the bias voltage generating circuit, and the oscillation frequency control signal from the loop filter is input to a gate of the transistor of the linearization circuit.

It appears that the Examiner is equating transistor X4 with the transistor of the linearization circuit recited in claim 14, and equates transistor X1, X2 with the bias voltage

generating circuit recited in the claim. However, Mamatz does not disclose that the source voltage of the transistor X4 is controlled to be the bias voltage of transistors X1, X2.

Accordingly, as anticipation under 35 U.S.C. § 102 requires that each element of the claim in issue be found, either expressly described or under principles of inherency, in a single prior art reference, *Kalman v. Kimberly-Clark Corp.*, 713 F.2d 760, 218 USPQ 781 (Fed. Cir. 1983), and Momtaz fails to disclose at least the above described elements, it is clear that Momtaz does not anticipate the independent claims.

Claims 13 and 15 – 17 depend from one of the independent claims. Under Federal Circuit guidelines, a dependent claim is nonobvious if the independent claim upon which it depends is allowable because all the limitations of the independent claim are contained in the dependent claims, *Hartness International Inc. v. Simplimatic Engineering Co.*, 819 F.2d at 1100, 1108 (Fed. Cir. 1987). Accordingly, as claims the independent claims are patentable for the reasons set forth above, it is respectfully submitted that all dependent claims are also in condition for allowance.

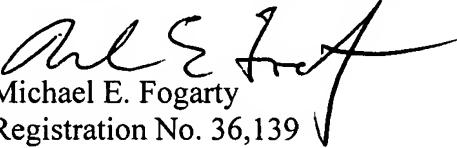
IV. Conclusion

In view of the above amendments and remarks, Applicants submit that this application should be allowed and the case passed to issue. If there are any questions regarding this Amendment or the application in general, a telephone call to the undersigned would be appreciated to expedite the prosecution of the application.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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